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Substitute for form 1469A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)		Complete if Known	
		Application Number	09/467992
		Filing Date	December 20, 1999
		First Name of Inventor	Forbes, Leonard
		Group Art Unit	2815
		Examiner Name	Lee, Eugene
Sheet 1 of 1		Attorney Docket No: 303.389US2	

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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EL	US-5,933,717	08/03/1999	Hause, Frederick N., et al.	438	200	03/04/1997
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Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
EL	EP-0198590	10/22/1986	Wada, M.	H01L	27/10	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
EL		WOLF, STANLEY , "Isolation Technologies for Intergrated Circuits", <u>Silicon Processing for the NLSI Era Vol. 2 Process Integration</u> , (1990),66-78	

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Substitute Disclosure Statement Form (PTO-1449)

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